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TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. §371			U.S. APPLICATION NO. (if known, use 37 CFR 1.51) NEW 09/787284
International Application No. PCT/JP00/04689	International Filing Date July 13, 2000	Priority Date Claimed July 15, 1999	
Title of Invention AV DECODER CONTROL METHOD AND AV DECODER CONTROL APPARATUS			
Applicant(s) For DO/EO/US Tomoyasu AMANO			
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:			
1. <input checked="" type="checkbox"/> This is a FIRST submission of items concerning a filing under 35 U.S.C. §371. 2. <input type="checkbox"/> This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. §371. 3. <input checked="" type="checkbox"/> This express request to begin national examination procedures (35 U.S.C. §371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. §371(b) and PCT Articles 22 and 39(1). 4. <input type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date. 5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. §371(c)(2)) a. <input type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau). b. <input checked="" type="checkbox"/> has been transmitted by the International Bureau. ATTACHMENT A c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US) 6. <input checked="" type="checkbox"/> A translation of the International Application into English (35 U.S.C. §371(c)(2)). ATTACHMENT B 7. <input type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. §371(c)(3)). a. <input type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau). b. <input type="checkbox"/> have been transmitted by the International Bureau. c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired. d. <input type="checkbox"/> have not been made and will not be made. 8. <input type="checkbox"/> A translation of the amendments to the claims under PCT Article 19. 9. <input checked="" type="checkbox"/> An oath or declaration (unexecuted) of the inventor(s) (35 U.S.C. §371(c)(4)). ATTACHMENT C 10. <input type="checkbox"/> A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. §371(c)(5)). Items 11. to 14. below concern other document(s) or information included: 11. <input checked="" type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98. ATTACHMENT D 12. <input type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included. 13. <input type="checkbox"/> A FIRST preliminary amendment. <input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment. 14. <input checked="" type="checkbox"/> Other items or information: Notification Concerning Submission or Transmittal of Priority Document - ATTACHMENT E			

U.S. APPLICATION NO. 09/787284 NEW	INTERNATIONAL APPLICATION NO. PCT/JP00/04689	ATTORNEY'S DOCKET NO. 2001-0287A
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15. ☒ The following fees are submitted

BASIC NATIONAL FEE (37 CFR 1.492(a)(1)-(5)):

Neither international preliminary examination fee nor international search fee paid to USPTO and International Search Report not prepared by the EPO or JPO \$1000.00

International Search Report has been prepared by the EPO or JPO \$ 860.00

International preliminary examination fee not paid to USPTO but international search paid to USPTO \$ 710.00

International preliminary examination fee paid to USPTO but claims did not satisfy provisions of PCT Article 33(1)-(4) \$ 690.00

International preliminary examination fee paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) \$ 100.00

ENTER APPROPRIATE BASIC FEE AMOUNT = \$860.00

Surcharge of \$130.00 for furnishing the oath or declaration later than ☐ 20 ☐ 30 months from the earliest claimed priority date (37 CFR 1.492(e)). \$

Claims	Number Filed	Number Extra	Rate	
Total Claims	12 -20 =		X \$18.00	\$
Independent Claims	2 - 3 =		X \$80.00	\$
Multiple dependent claim(s) (if applicable)			+ \$270.00	\$
TOTAL OF ABOVE CALCULATIONS =				\$860.00
<input type="checkbox"/> Small Entity Status is hereby asserted. Above fees are reduced by 1/2.				\$
SUBTOTAL =				\$860.00
Processing fee of \$130.00 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f)).				\$
TOTAL NATIONAL FEE =				\$860.00
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40 per property +				\$
TOTAL FEES ENCLOSED =				\$860.00
				Amount to be refunded \$
				Amount to be charged \$

a. ☒ A check in the amount of \$860.00 to cover the above fees is enclosed. A duplicate copy of this form is enclosed.

b. ☐ Please charge my Deposit Account No. 23-0975 in the amount of \$_____ to cover the above fees. A duplicate copy of this sheet is enclosed.

c. ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 23-0975.

NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

19. CORRESPONDENCE ADDRESS



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March 15, 2001

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DESCRIPTION

AV DECODER CONTROL METHOD AND AV DECODER CONTROL APPARATUS

5 TECHNICAL FIELD

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The present invention relates to an AV (Audio Visual)
decoder control method and an AV decoder control apparatus
which are used for an AV decoding/playback apparatus
having an AV decoder for decoding a bit stream including
10 compressed video data and audio data to play back video
and audio. More particularly, the present invention
relates to an AV decoder control method and an AV decoder
control apparatus for trick play in which I pictures
(Intra-frame coded pictures) of video data are
15 continuously displayed by an AV decoder.

BACKGROUND ART

With the development of digital technology in recent
years, playback methods using recording media such as
20 optical disks and magnetic disks have been put to
practical use for playback of digitized sources, and
various kinds of data compression/ multiplexing methods
have been proposed. MPEG (Moving Picture Experts Group)
is one of these methods, and especially MPEG1 is used for
25 video CD. In MPEG1, I pictures are included in GOP (group
of pictures), and each I picture maintains its
independence by itself, differently from B pictures
(Bidirectionally predictive coded pictures) and P pictures
(Predictive coded pictures). By continuously decoding the

I pictures (hereinafter referred to as I playback), a trick play such as fast forward playback (i.e., forward I playback) or fast reverse playback (i.e., backward I playback) is achieved.

5 However, since no navigation information is included in a video CD while it is included in a DVD (Digital Versatile Disc), the position of GOP which appears in a bit stream cannot be predicted. Therefore, in an AV decoding/playback apparatus such as a video CD player, in
10 order to perform I playback, a bit stream is divided into unit lengths without considering GOP included in the bit stream, and thus obtained bit streams are sequentially input to the AV decoder for decoding, and I pictures included in each bit stream are sequentially displayed.

15 More specifically, in the conventional I playback, a bit stream is divided into plural bit streams having the same bit length, and these divided bit streams are sequentially read from the disk and input to the AV decoder for decoding. Every time an I picture appears,
20 the I picture is displayed. When decoding of one divided bit stream is completed, the following divided bit stream is input to the AV decoder.

 In the conventional I playback, however, the time required for moving the playback point (seek point) on the
25 bit stream in the I playback by one divided bit length is equal to the time required for reading and decoding one divided bit stream and, therefore, the time for moving the playback point on the bit stream for fast-forward playback or fast-reverse playback is increased. As the result,

high-speed forward playback or reverse playback cannot be achieved.

The present invention is made to solve the above-described problems and has for its object to provide an AV
5 decoder control method and an AV decoder control apparatus which can perform high-speed I playback using an AV decoder.

DISCLOSURE OF THE INVENTION

10 In an AV decoder control method according to the present invention, a plurality of bit streams having a predetermined bit length and positioned at arbitrary intervals, which are obtained by dividing a bit stream including MPEG video data and audio data, are separately
15 and sequentially input to an AV decoder and decoded, and I pictures of the video data included in these bit streams of the predetermined bit length are continuously displayed while updating the I pictures. Therefore, in I playback, the time required for moving the playback point (seek
20 point) on each bit stream by a distance that is the sum of the arbitrary interval and the predetermined bit length, becomes equal to the time required for decoding the bit stream of the predetermined bit length, thereby providing an AV decoder control method which permits the AV decoder
25 to perform high-speed I playback.

Further, in the above-described AV decoder control method, display of the I picture which is currently displayed is continued until the displayed I picture is updated. Therefore, even when the bit stream includes

errors, block noise and flashing screen due to decoding of insignificant data are avoided.

Further, in the above-described AV decoder control method, when inputting a bit stream of the predetermined
5 bit length to the AV decoder, a previous bit stream of the predetermined bit length, which has been input to the AV decoder most recently and stored in a buffer in the AV decoder, is erased. Therefore, the buffer in the decoder is maintained in the normal state, whereby abnormal
10 display is avoided.

Further, in the above-described AV decoder control method, it is decided whether display of I pictures in a bit stream of the predetermined bit length by the AV decoder has been completed or not, and when the display of
15 I pictures has been completed, a next bit stream of the predetermined bit length, which follows the bit stream for which display of I pictures has ended, is input to the AV decoder. Therefore, the AV decoder can perform high-speed I display.

20 Further, in the above-described AV decoder control method, a plurality of bit streams which are obtained by dividing each of the bit streams having the predetermined bit length, are sequentially input to the AV decoder at predetermined intervals, and every time one divided bit
25 stream is input to the AV decoder, it is decided whether display of I pictures by the AV decoder has been completed or not. When display of I pictures has not been completed yet, a next divided bit stream within the same bit stream is input to the AV decoder. On the other hand, when

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5 perform high-speed I playback.

10 bit length is optimized using this result. Therefore, the
AV decoder can perform high-speed I playback.

15 plurality of bit streams having a predetermined bit length
and positioned at arbitrary intervals, which are
separately and sequentially input to the AV decoder, and
continuously displays I pictures included in these bit
streams while sequentially updating the I pictures.

20 Therefore, in I playback, the time required for moving the
playback point (seek point) on each bit stream by a
distance that is the sum of the arbitrary interval and the
predetermined bit length, becomes equal to the time
required for decoding the bit stream of the predetermined
25 bit length, thereby providing an AV decoder control method
which permits the AV decoder to perform high-speed I
playback.

Further, the above-described AV decoder control apparatus includes means for continuing display of the I

picture which is currently displayed until the displayed I picture is updated. Therefore, even when the bit stream includes errors, block noise and flashing screen due to decoding of insignificant data are avoided.

5 Further, the above-described AV decoder control apparatus includes means for, when inputting a bit stream of the predetermined bit length to the AV decoder, erasing a previous bit stream of the predetermined bit length, which has been input to the AV decoder most recently and
10 stored in a buffer in the AV decoder. Therefore, the buffer in the decoder is maintained in the normal state, whereby abnormal display is avoided.

Further, the above-described AV decoder control apparatus includes means for deciding whether display of I pictures in a bit stream of the predetermined bit length by the AV decoder has been completed or not, and when display of I pictures has been completed, this means inputs, to the AV decoder, a next bit stream of the predetermined bit length that follows the bit stream for which display of I pictures has ended. Therefore, the AV decoder can perform high-speed I playback.

Further, the above-described AV decoder control apparatus includes means for sequentially inputting a plurality of bit streams which are obtained by dividing 25 each of the bit streams having the predetermined bit length, to the AV decoder at predetermined intervals, and for deciding whether display of I pictures by the AV decoder has been completed or not every time one divided bit stream is input. When display of I pictures has not

been completed, this means inputs a next divided bit stream within the same bit stream to the AV decoder. On the other hand, when display of I pictures has been completed, this means inputs, to the AV decoder, a next
 5 bit stream of the predetermined bit length that follows the bit stream for which display of I pictures has ended. Therefore, the AV decoder can perform high-speed I playback.

Further, the above-described AV decoder control
 10 apparatus includes means for obtaining an optimum bit length of the bit streams having the predetermined bit length, from repetition of inputting these bit stream to the AV decoder, and for optimizing the bit length by using this result. Therefore, the AV decoder can perform high-
 15 speed I playback.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a block diagram illustrating a video CD player, for explaining an AV decoder control method and an
 20 AV decoder control apparatus according to a first embodiment of the present invention.

Figure 2 is a diagram illustrating an MPEG1 stream, for explaining the AV decoder control method and the AV decoder control apparatus according to the first
 25 embodiment.

Figure 3 is a flowchart for explaining the AV decoder control method and the AV decoder control apparatus according to the first embodiment.

Figure 4 is a flowchart for explaining an AV decoder

control method and an AV decoder control apparatus according to a second embodiment of the present invention.

Figure 5 is a block diagram illustrating a video CD player, for explaining the AV decoder control method and the AV decoder control apparatus according to the second embodiment.

BEST MODE TO EXECUTE THE INVENTION

Embodiment 1.

Figure 1 is a block diagram illustrating a video CD player, for explaining an AV decoder control method and an AV decoder control apparatus according to a first embodiment of the present invention. In figure 1, an input unit 100 is composed of, for example, a remote controller having at least one button and a control program thereof. When the user pushes the button, the input unit 100 outputs the corresponding signal. A playback controller 101 is implemented by a CPU (Central Processing Unit), a main storage such as a semiconductor memory, and control programs thereof. The playback controller receives the signal from the input unit 100, and controls a drive controller 102 and an AV decoder controller 103. The drive controller 102 is implemented by a CPU, a main storage such as a semiconductor memory, and control programs thereof. The drive controller 102 controls a drive 104 according to an instruction from the playback controller 101. The AV decoder controller 103 is implemented by a CPU, a main storage such as a semiconductor memory, and control programs thereof. The

AV decoder controller 103 controls an AV decoder 105 according to an instruction from the playback controller 101. The drive 104 reads data from an optical disk. The AV decoder 105 is implemented by a buffer for temporarily storing part of a bit stream before decoded, a video decoder, and an audio decoder, which decoders perform inverse DCT (Discrete Cosine Transform) on video packets and audio packets to decode these packets. The AV decoder 105 decodes the video packets and audio packets taken from the drive 104 to obtain video data and audio data. An output unit 106 is implemented by a display and a speaker, or the like, and outputs the video data and audio data decoded by the AV decoder 105.

Figure 3 is a flowchart illustrating the flow of control when the video CD player performs I playback, for explaining the AV decoder control method and the AV decoder control apparatus according to the first embodiment.

Further, figure 2 is a diagram illustrating the structure of an MPEG1 stream having video packets and audio packets to be subjected to I playback, for explaining the AV decoder control method and the AV decoder control apparatus according to the first embodiment.

Hereinafter, the operation of the video CD player during I playback will be described with reference to figures 1 to 3.

It is assumed that the MPEG1 stream shown in figure 2 is currently being played back normally, and the user will

switch the operation at point P03 from normal playback to reverse playback, i.e., backward I playback.

In step S11, the playback controller 101 waits for the user's inputting an instruction to the input unit 100.

5 When there is an input, the control proceeds to step S12. In step S12, it is decided whether the user's instruction is I playback or not. When it is I playback, the control proceeds to step S13. When it is not I playback, the control proceeds to END step. In step S13, the playback controller 101 instructs the AV decoder controller 103 to clear the contents of the buffer of the AV decoder 105. The AV decoder controller 103 clears the buffer of the AV decoder 105. Then, the control proceeds to step S14.

In step S14, the playback controller 101 instructs the drive controller 102 to seek a specific address. On receipt of the instruction from the playback controller 101, the drive controller 102 controls the drive 104 so that it seeks the specific address. In order to perform backward I playback from the point P03, the seek point should be turned back from the point P03 by a predetermined step width of the bit stream. In this first embodiment, the step width is an arbitrary width larger than 80 blocks. On receipt of the instruction from the playback controller 101, the drive controller 102 seeks the drive 104 at the point P02.

Subsequently, decoding of the bit stream having a data length of 80 blocks is performed from the position P02 in the bit stream progressing direction, i.e., in the forward direction. In this first embodiment, when

performing decoding of the 80-block bit stream, this 80-block bit stream is divided into continuous plural bit streams having a predetermined bit length, e.g., bit streams each having a data length of 16 blocks, and these
 5 16-block bit streams are sequentially input to the AV decoder 105 from the P02 side at predetermined time intervals.

Initially, in step S15, the playback controller 101 instructs the drive controller 102 to read 16 blocks. On
 10 receipt of the instruction from the playback controller 101, the drive controller 102 controls the drive 104 so that it reads the 16 blocks. Then, the control proceeds to step S16. In step S16, the playback controller 101 instructs the AV decoder controller 103 to start transfer
 15 of the bit stream which has been read in step S15. On receipt of the instruction from the playback controller 101, the AV decoder controller 103 controls the AV decoder 105 so that it starts the process of transferring the read bit stream from the drive 104 to the AV decoder 105, and
 20 outputting the video data and audio data obtained from the drive 104 to the output unit 106. The AV decoder 105 starts decoding under control of the AV decoder controller 103. When an I picture appears, this I picture is sent to the output unit 106 for display. Display of this I
 25 picture is continued until the next I picture is displayed.

Next, the control proceeds to step S17. In step S17, the playback controller 101 detects the elapsed time from when the playback controller 101 performs seek of the drive 104 to the present time. When the time has reached

a predetermined time, the control proceeds to step S18. When it has not reached the predetermined time yet, the control proceeds to step S19.

In step S19, the playback controller 101 decides
5 whether or not there is an information from the AV decoder 105 that the AV decoder 105 has performed I-picture switching. When the I picture has been switched, the control proceeds to step S17. When the I picture has not been switched, the control proceeds to step S20.

10 In step S20, the playback controller 101 decides whether or not the drive 104 has performed reading of predetermined blocks corresponding to one seek. When reading of the predetermined blocks has been performed, the control proceeds to step S17. In this case, the
15 predetermined blocks for one seek are 80 blocks as described above, and only 16 blocks have been read from the seek point P02 and, therefore, the control returns to step S15.

The process steps from S15 to S20 are repeated, and
20 when 80 blocks have been read from the point P02 shown in figure 2 or when display of the next I picture has been completed, the playback controller 101 decides whether the elapsed time from when the drive 104 performs seek to the present time exceeds the predetermined time or not, in
25 step S17. When the elapsed time exceeds the predetermined time, it is decided in step S18 whether I playback should be ended or not. When seek does not reach the end point P01 of the bit stream and there is no instruction to end I playback, the control proceeds to step S13, and new seek

is performed from the current seek point P02.

In this way, the above-described process steps are repeated, and when seek for I playback reaches the point P01 at the beginning of the bit stream, the control
5 proceeds to step S18 to end I playback.

In this first embodiment, the interval on the bit stream between each seek point in reverse playback and the point immediately before the seek is larger than 80 blocks, and 80 blocks of data are decoded from each seek point in
10 the forward direction, and I pictures included in the data are displayed. Therefore, decoding for reverse playback is performed by sequentially inputting a plurality of 80-block bit streams which are positioned at predetermined intervals, in the AV decoder 105, starting from the
15 reverse playback start position. As the result, the time required for moving the seek (playback) point on the bit stream in I playback by a distance equivalent to one seek is equal to the time for reading and decoding less blocks of data than the data blocks included in one seek, i.e.,
20 the time for reading and decoding 80 blocks of data.

Accordingly, in this first embodiment, the time required for moving the seek point on the bit stream in I playback by a distance equivalent to one seek can be reduced as compared with the time for reading and decoding the data
25 of the bit length for one seek in the conventional I playback, i.e., the time for reading and decoding all of the data having the bit length longer than 80 blocks. Therefore, the time for moving the seek point on the bit stream in fast forward playback and reverse playback can

be reduced, resulting in high-speed I playback.

Further, when display of I pictures is completed before decoding of 80 blocks of data for one seek is completed, the decoder 105 informs the end of display to the AV decoder controller 103. On receipt of this, the AV decoder controller 103 decides to end the display, and starts next seek. So, the next seek can be started before decoding all of the 80 blocks, whereby the time for moving the seek point on the bit stream is further reduced.

Further, when performing seek on the bit stream in the backward direction, the buffer of the decoder 105 is cleared whether I picture display is updated or not. Therefore, the bit stream inputted to the AV decoder 105 is always continuous from the sought position.

Accordingly, in the case where the bit stream of the previous seek remains in the buffer, if the bit stream from the next seek position is inputted to the buffer, discontinuous bit stream occurs, resulting in abnormal display such as separated images. In this first embodiment, however, since the buffer of the decoder is maintained in the normal state, such discontinuous bit stream does not occur, thereby avoiding abnormal display. Further, even when the buffer is being cleared, the previous I picture is continuously displayed. So, even when the bit stream includes errors, block noise and flashing screen due to decoding of insignificant data are avoided.

While in this first embodiment the bit length of the bit stream to be decoded in one seek is 80 blocks, the bit

length is not restricted to 80 blocks, and any bit length may be employed so long as I playback is possible.

Embodiment 2.

5 Figure 5 is a block diagram illustrating a video CD player, for explaining an AV decoder control method and an AV decoder control apparatus according to a second embodiment of the present invention. This video CD player includes a playback controller 110 which obtains an
10 optimum bit length from the results of repetition of inputting bit streams having a predetermined bit length to the AV decoder 105, and thereby optimizes the bit length of bit streams to be decoded by the AV decoder 105. In the figure, the same reference numerals as those shown in
15 figure 1 denote the same or corresponding parts.

 Figure 4 is a flowchart illustrating the flow of control when the video CD player performs I playback, for explaining the AV decoder control method and the AV decoder control apparatus according to this second
20 embodiment.

 Hereinafter, the operation of the CD player during I playback will be described with reference to figure 4. In this second embodiment, as in the first embodiment, it is assumed that the user performs fast reverse playback as
25 trick play during playback of a bit stream having the structure shown in figure 2. In figure 4, the same step numbers as those shown in figure 3 denote the same or corresponding processes.

 In this second embodiment, the number of blocks in

the bit stream to be decoded in one seek, which is determined in step S20 of figure 4, is not fixed but dynamically updated by learning, and the number of blocks is determined in step S21.

5 In step S20, the playback controller 101 decides whether or not the drive 104 has read predetermined blocks during decoding for the current seek. When the drive 104 has read the predetermined blocks, the control proceeds to step S17. When the drive 104 has not read the blocks yet,
10 the control returns to step S15.

 In step S21, the number of predetermined blocks to be decided in step S20 is optimized. This optimization is performed as follows. When display of I pictures by the AV decoder 105 is performed normally, the number of blocks
15 which have been read by the drive 104, which are required until the previous I picture display is completed, is compared with the number of blocks required until the current display is completed, and the smaller one is adopted as the number of predetermined blocks to be used
20 in step S20. Next, the control proceeds to step S18. When end of I playback is confirmed by end of the bit stream or another request from the user, I playback is ended. When end of I playback is not confirmed, the control returns to step S13 and the above-described
25 process steps are repeated.

 As described above, according to the second embodiment of the present invention, the same effects as those provided by the first embodiment are achieved. Moreover, since the length of the bit stream to be decoded

in one seek is optimized in accordance with the bit stream to be played back, decoding of data other than I pictures is reduced during I playback, resulting in higher-speed I playback. Further, the throughput of the whole system can
5 be improved, and the system can rapidly respond to user's request.

While in the first and second embodiments fast reverse playback is described, the present invention can be applied to fast forward playback so long as it is I
10 playback. Also during fast forward playback, by sequentially decoding a plurality of bit streams having a predetermined bit length and positioned at regular intervals from the playback starting position, the same effects as those provided by the first and second
15 embodiments are achieved.

While in the first and second embodiments video CD players are employed, the present invention can be applied to other AV decoding/playback apparatuses with the same effects as those provided by the first and second
20 embodiments.

APPLICABILITY IN INDUSTRY

As described above, an AV decoder control method and an AV decoder control apparatus according to the present
25 invention are available for trick play such as fast forward playback or fast reverse playback and, particularly, they are suitable for a video CD player which employs MPEG1 as a video data compression/multiplexing method.

CLAIMS

1. An AV decoder control method for controlling an AV decoder included in an AV decoding/playback apparatus
5 which decodes and plays back a bit stream including MPEG video data and audio data, said method controlling the AV decoder so that it performs a trick play by continuously displaying I pictures of the MPEG video data:

wherein a plurality of bit streams having a
10 predetermined bit length and positioned at arbitrary intervals are separately and sequentially input to the AV decoder and decoded, and I pictures included in these bit streams are continuously displayed while sequentially updating the I pictures.

15

2. The AV decoder control method of Claim 1 wherein display of the I picture which is currently displayed is continued until the displayed I picture is updated.

20 3. The AV decoder control method of Claim 1 wherein, when inputting a bit stream of the predetermined bit length to the AV decoder, a previous bit stream of the predetermined bit length, which has been input to the AV decoder most recently and stored in a buffer in the AV decoder, is
25 erased.

4. The AV decoder control method of Claim 1 wherein:
it is decided whether display of I pictures in a bit stream of the predetermined bit length by the AV decoder

has been completed or not; and

when the display of I pictures has been completed, a next bit stream of the predetermined bit length, which follows the bit stream for which display of I pictures has ended, is input to the AV decoder.

5. The AV decoder control method of Claim 1 wherein:

a plurality of bit streams which are obtained by dividing each of the bit streams having the predetermined bit length, are sequentially input to the AV decoder at predetermined intervals;

every time one divided bit stream is input to the AV decoder, it is decided whether display of I pictures by the AV decoder has been completed or not;

when display of I pictures has not been completed yet, a next divided bit stream within the same bit stream is input to the AV decoder; and

when display of I pictures has been completed, a next bit stream of the predetermined bit length, which follows the bit stream for which display of I pictures has ended, is input to the AV decoder.

6. The AV decoder control method of Claim 1 wherein an optimum bit length of the bit streams having the predetermined bit length is obtained from repetition of inputting these bit stream to the AV decoder, and the bit length is optimized using this result.

7. An AV decoder control apparatus for controlling an AV

decoder included in an AV decoding/playback apparatus which decodes and plays back a bit stream including MPEG video data and audio data, said apparatus controlling the AV decoder so that it performs a trick play by

5 continuously displaying I pictures of the video data, said apparatus comprising:

means for controlling the AV decoder so that the AV decoder decodes a plurality of bit streams having a predetermined bit length and positioned at arbitrary

10 intervals, which are separately and sequentially input to the AV decoder, and continuously displays I pictures included in these bit streams while sequentially updating the I pictures.

15 8. The AV decoder control apparatus of Claim 7 further comprising means for continuing display of the I picture which is currently displayed until the displayed I picture is updated.

20 9. The AV decoder control apparatus of Claim 7 further comprising means for, when inputting a bit stream of the predetermined bit length to the AV decoder, erasing a previous bit stream of the predetermined bit length, which has been input to the AV decoder most recently and stored

25 in a buffer in the AV decoder.

10. The AV decoder control apparatus of Claim 7 further comprising means for deciding whether display of I pictures in a bit stream of the predetermined bit length

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SUMMARY

There is provided an AV decoder control method and an AV decoder control apparatus for controlling an AV decoder
5 so that it performs a trick play by continuously displaying I picture of MPEG video data, wherein a plurality of bit streams having a predetermined bit length and positioned at arbitrary intervals are sequentially and separately input to the AV decoder, and I pictures
10 included in these bit streams having the predetermined bit length are continuously displayed while sequentially updating the I pictures, whereby high-speed I playback is realized.

Fig.1

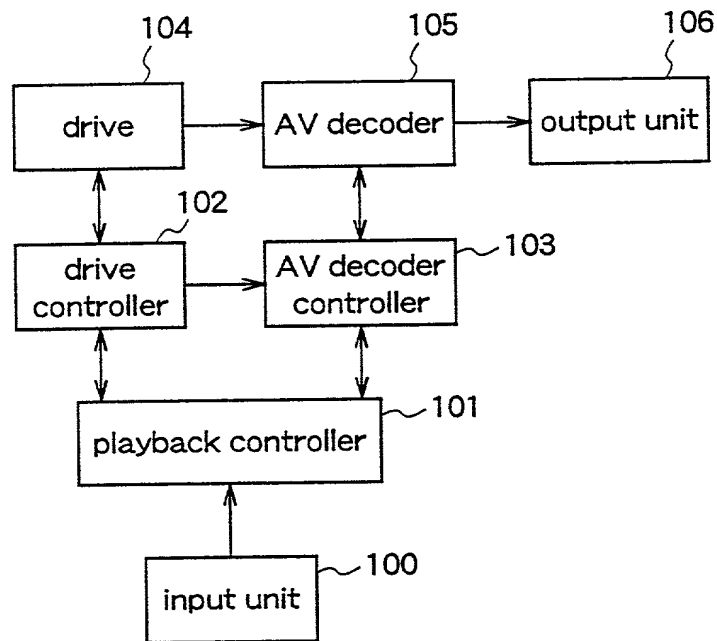


Fig.2

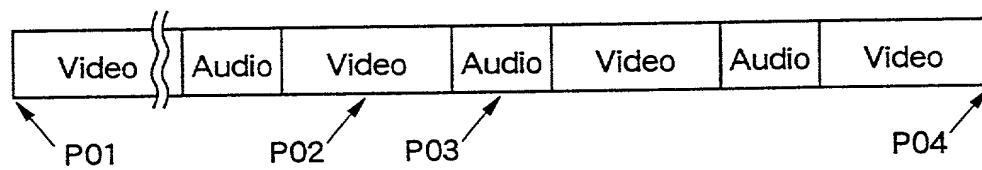


Fig.3

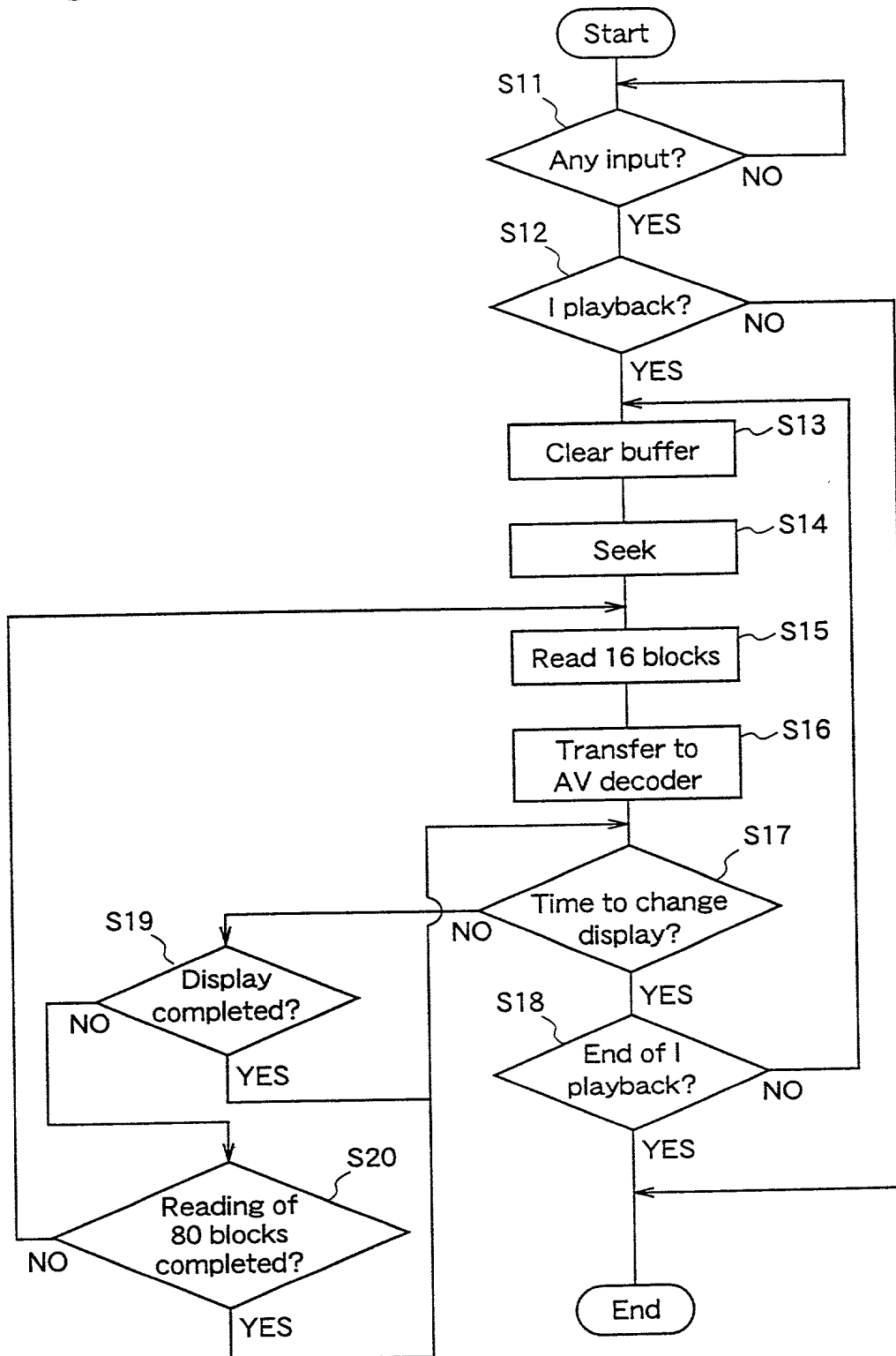


Fig.4

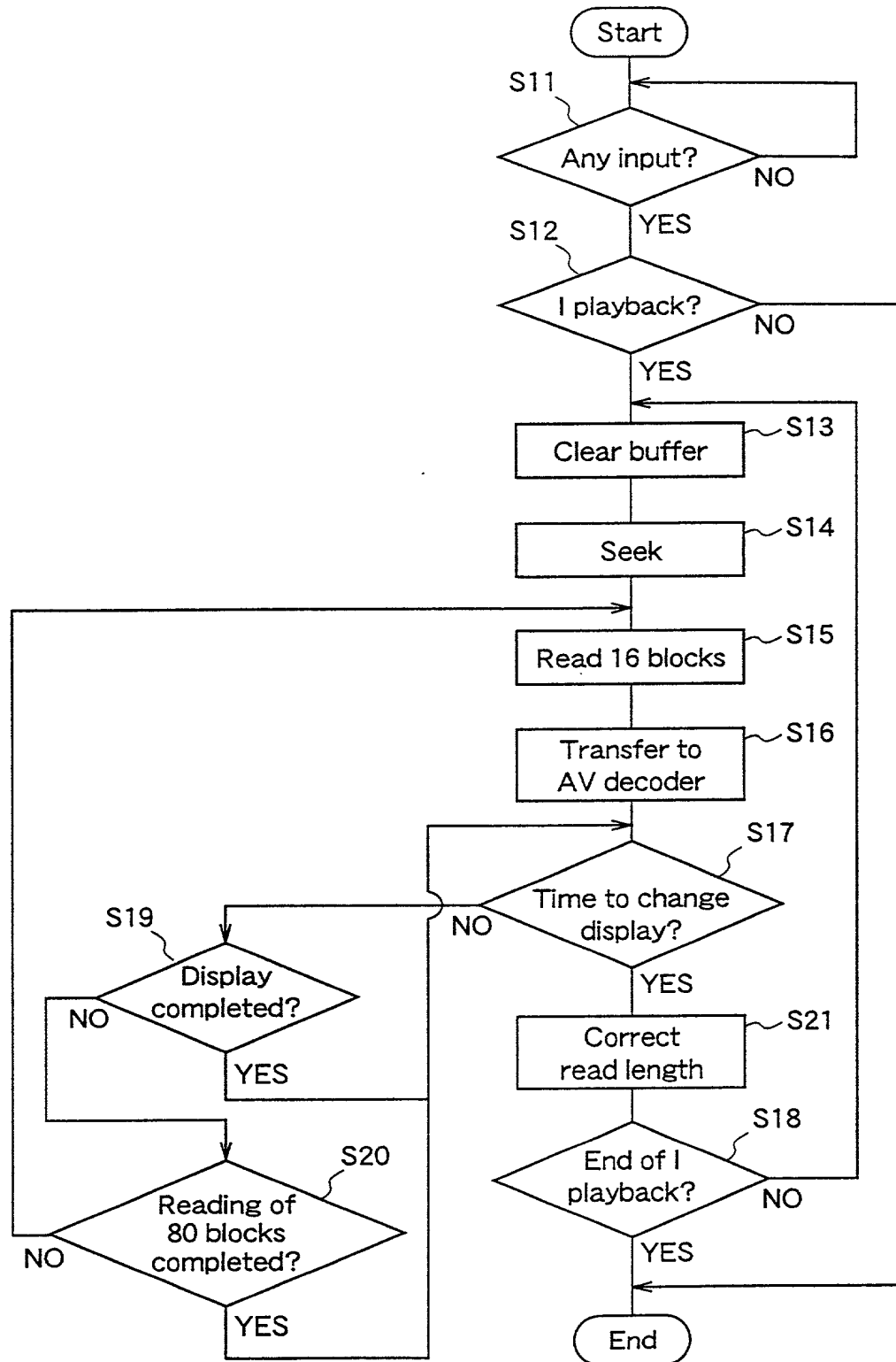
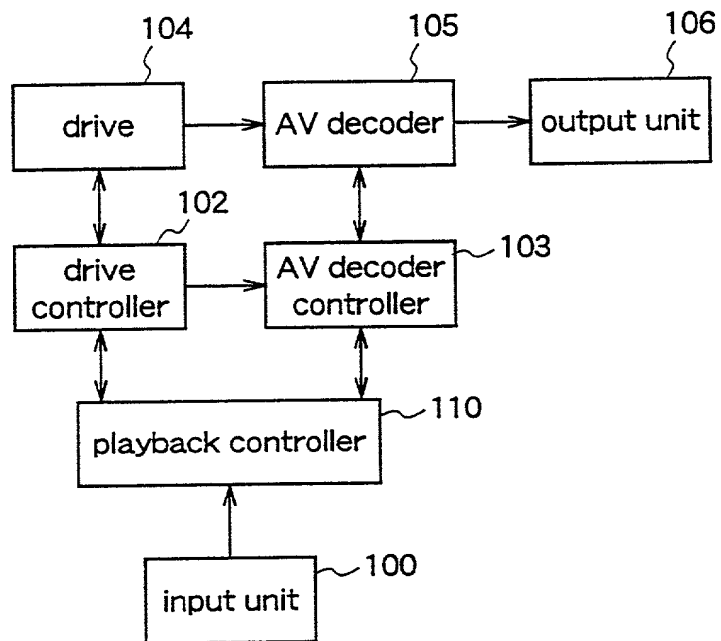


Fig.5



DECLARATION AND POWER OF ATTORNEY FOR U.S. PATENT APPLICATION

() Original () Supplemental () Substitute (X) PCT () DESIGN

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Title: AV DECODER CONTROL METHOD AND AV DECODER CONTROL APPARATUS

of which is described and claimed in:

- () the attached specification, or
 () the specification in application Serial No. NEW, filed March 15, 2001, and with amendments through _____, or
 (X) the specification in International Application No. PCT/JP00/04689, filed July 13, 2000, and as amended on (if applicable).

I hereby state that I have reviewed and understand the content of the above-identified specification, including the claims, as amended by any amendment(s) referred to above.

I acknowledge my duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim priority benefits under Title 35, United States Code, §119 (and §172 if this application is for a Design) of any application(s) for patent or inventor's certificate listed below and have also identified below any application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:


COUNTRY	APPLICATION NO.	DATE OF FILING	PRIORITY CLAIMED
Japan	No.Hei.11-201794	July 15, 1999	YES

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION SERIAL NO.	U.S. FILING DATE	STATUS: PATENTED, PENDING, ABANDONED

And I hereby appoint Michael R. Davis, Reg. No. 25,134; Matthew M. Jacob, Reg. No. 25,154; Warren M. Cheek, Jr., Reg. No. 33,367; Nils Pedersen, Reg. No. 33,145; Charles R. Watts, Reg. No. 33,142; and Michael S. Huppert, Reg. No. 40,268, who together constitute the firm of WENDEROTH, LIND & PONACK, L.L.P., as well as any other attorneys and agents associated with Customer No. 000513, to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys and agents named herein to accept and follow instructions from HAYASE & CO. as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

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Post Office Address	ADDRESS	CITY	STATE OR COUNTRY ZIP CODE

I further declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

1st Inventor Tomoyasu Amano Date May 17, 2001
Tomoyasu AMANO
2nd Inventor _____ Date _____
3rd Inventor _____ Date _____
4th Inventor _____ Date _____
5th Inventor _____ Date _____
6th Inventor _____ Date _____

The above application may be more particularly identified as follows:

U.S. Application Serial No. NEW Filing Date March 15, 2001

Applicant Reference Number P-22850-02 Atty Docket No. 2001-0287A

Title of Invention AV DECODER CONTROL METHOD AND AV DECODER CONTROL APPARATUS